

**Amendments to the Specification:**

On page 1, please amend paragraph [0001] as follows:

[0001] The present application is related to U.S. Patent Application Serial No. 10/689,335 [[\_\_\_\_\_]] entitled "Method for Finding Local Extrema of a Set of Values for a Parallel Processing Element" filed [[\_\_\_\_\_]] ~~(DB001076-000, Micon no. 03-0052)~~ 20 October 2003, and U.S. Patent Application Serial No. 10/689449 [[\_\_\_\_\_]] entitled "Method for Finding Global Extrema of a Set of Shorts Distributed Across an Array of Parallel Processing Elements" filed [[\_\_\_\_\_]] ~~(DB001078-000, Micon no. 03-0054)~~ 20 October 2003.

On page 9, please amend paragraph [0050] as follows:

The reader desiring more information about the hardware shown in FIGs. 1 - 3 is directed to UK Patent application 0221563.0 ~~(serial no. not yet assigned)~~ entitled "Control of Processing Elements in Parallel Processors" filed 17 September 2002, ~~(Micon no. 02-1604)~~ which is hereby incorporated by reference. Details about the PEs may also be found in UK Patent Application No. 021562.2 entitled "Host Memory Interface for a Parallel Processor" filed 17 September 2002, ~~(Micon no. 02-0703)~~ which is hereby incorporated by reference.

On pages 9 – 10, please amend paragraph [0052] as follows:

[0052] For example in the current embodiment, each PE in array 28 (see FIG. 7) receives a set of values from the DRAM interface 44 and the host memory access port 46 (see FIG. 1), among others. After the values are assigned to each PE in the array 28, each PE determines its local extrema. In the current embodiment, local extrema refers to the maximum or minimum value for a set of values assigned to an individual PE. A method for determining a local extrema from a set of values on an individual PE is discussed in more detail in U.S. Patent Application Serial No. 10/689,335 [[\_\_\_\_\_]] entitled "Method for Finding Local Extrema of a Set of Values for a Parallel Processing Element" filed [[\_\_\_\_\_]] ~~(DB001076-000, Micon no. 03-0052)~~ 20 October 2003 and incorporated in its entirety by reference herein.